

2826



10/010,343  
LETTER TO THE OFFICIAL DRAFTSPERSON

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  Visvamohan Yegnashankaran et al.  Appln. No.: 10/010,343  Filed: December 05, 2001  For: INTEGRATED CIRCUIT AND METHOD OF FORMING THE INTEGRATED CIRCUIT HAVING A DIE WITH HIGH Q INDUCTORS AND CAPACITORS ATTACHED TO A DIE WITH A CIRCUIT AS A FLIP CHIP	Group Art Unit: 2826  Examiner: L. Andújar  LETTER TO THE OFFICIAL DRAFTSPERSON  RECEIVED MAY 29 2003 TECHNOLOGY CENTER 2810
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

Dear Sir:

With the permission of the Examiner, please approve the proposed drawing changes shown in red on the attached copy of FIG. 1.

Dated: 5-21-03 By: Mark C. Pickering  
Respectfully submitted,  
Mark C. Pickering  
Registration No. 36,239  
Attorney for Assignee

P.O. Box 300  
Petaluma, CA 94953-0300  
Direct Dial Telephone No. (707) 762-5583  
Telephone: (707) 762-5500  
Facsimile: (707) 762-5504  
Customer No: 33402

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service, postage prepaid, in an envelope, addressed to Mail Stop \_\_\_\_\_, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 05-21-03

Dated: May 21, 2003 By: Philip H. King